REMARKS

Claims 1-7 are pending in the application.

Claims 2 and 3 are to contain allowable subject matter. Claim 1, 4-7 stand rejected.

Claims 1 and 7 have been amended herein to clarify the claimed invention. For example the claims describe: a hand over of internal information transmitted from a preceding-stage packet processor, and also transmit the internal information of its corresponding processor to a succeeding-stage packet processor. The internal information is not parameters which are generated by converting the internal status, but denotes information directly indicative of the internal execution status of the packet processor.

This amendment is based on the description of page 6, lines 12-17, page 6, lines 18-22 and Fig. 1. Also examples on page 10, line 25 through page 11, line 12, and page 13, line 13 through page 14, line 19.

Claims 1, 6 and 7 are rejected under 35 U.S.C.(a) as unpatentable over Bala (U.S. 6,335,992) in view of Hwang (U.S. 6,490,260).

Bala is relied on to teach most of the claimed features. However it's admitted that Bala fails to teach the internal information and the Office Action relies on the Hwang reference to show the computing of input packet in accordance with internal information.

It appears that the Office Action believes that the CRC bits in the Hwang reference are equivalent to applicant's claimed internal information.

Claim 4-5 are both rejected as unpatentable as applied to claim 1 with claim 4 being further being further in view of Phillips (U.S. 6,438,368) and claim 5 and further in view of Hwang.

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Hwang teaches a second CRC encoder 48 generates second CRC bits for detecting an error of the control channel frame which has been multiplexed with the first CRC bits of the traffic channel. The second CRC encoder 48 inserts the second CRC bits into the control channel frame. The structure of the control channel frame generated by second CRC encoder 48 is shown in FIG. 5. The control channel frame is comprised of C control and signaling information bits, F first CRC bits, M second CRC bits, and T tail bits.

Also the CRC bits for detecting an error in the user service information are transmitted using a physically independent control channel frame instead of a traffic channel frame. Therefore, the throughput of user service information can be increased by a number of bits equal to the CRC bits which have been eliminated from the traffic channel frame.

However in contrast to the CRC information applicant's claims recite internal information is not parameters which are generated by converting the internal status, but denotes information directly indicative of the internal execution status of the packet processor.

Also in applicant's claims the internal information is transmitted from a preceding-stage packet processor, and also transmited to a succeeding-stage packet processor.

The combination of Hwang and Bala fail to disclose the function of the internal information handover means as recited by claims 1 and 7. Accordingly, the invention set forth in claims 1 and 7 would not be obvious from a combination of Hwang and Bala since the combination fails to show or suggest all the claimed features.

Applicant's claims 4-6 depend from claim 1 and are allowable because of at least their dependence upon claim 1 and because they each recite additional features.

In view of the remarks set forth above, this application is in condition for allowance which action is respectfully requested. However, if for any reason the Examiner should consider

this application not to be in condition for allowance, the Examiner is respectfully requested to telephone the undersigned attorney at the number listed below prior to issuing a further Action.

Any fee due with this paper may be charged to Deposit Account No. 50-1290.

Respectfully submitted,

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